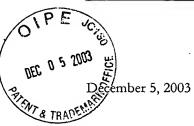
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U.S. Patent Application Serial No. 10/081,821 Re:

Inventor: Stockstad

Title: Integrated Circuit and Method of Controlling Output Impedance

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Dear Sir/Madam:

Enclosed herewith for filing in the above-identified application are the following:

1. Response to Office Action (5 pgs.);

2. Terminal Disclaimer to Obviate a Double Patenting Rejection Over A Prior

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3. Self-addressed, prepaid postcard to acknowledge receipt of documents.

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